3.3V HCMOS Surface Mount Crystal Clock Oscillator 7113, 7123, 7133, 7443

XO



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Description

The Connor-Winfield models 7113, 7123, 7133 and 7443 are a 5 x 3.2mm, 3.3V HCMOS, Surface Mount, Fixed Frequency Crystal Oscillators (XO) designed for use in all applications requiring precision clocks. These oscillators feature low stand-by current (10uA) when output is disabled. The RoHS compliant, surface mount package is designed for highdensity mounting and is optimum for mass production.

Features:

- 1.8 to 160 MHz
- 3.3V Operation
- Tri-State Enable / Disable Function
- Overall Frequency Tolerance:
 - 7113 ± 25 ppm; 7123 ± 50 ppm;
 - 7133 ± 100 ppm; 7443 ± 20 ppm
- Temperature Ranges: 0 to 70°C; -20 to 70°C
- Power Saving Function: 10uA When Disabled
- Ceramic Surface Mount Package
- Tape and Reel Packaging
- RoHS Compliant

Absolute Maximum Ratings

Parameter	Minimum	Nominal	Maximum	Units	Notes
Storage Temperature	-55	-	125	°C	
Supply Voltage (Vcc)	-0.5	-	7.0	Vdc	

Operating Specifications						
Parameter	Minimum	Nominal	Maximum	Units	Notes	
Frequency Range (Fo)	1.8	-	160	MHz		
Frequency Tolerance				ppm	1	
7113	-25	-	25			
7123	-50	-	50			
7133	-100	-	100			
7443	-20		20			
Operating Temp Range						
71xx models	0	-	70	°C		
74xx models	-20	-	70	°C		
Supply Voltage (Vdd)	3.0	3.3	3.6	Vdc		
Supply Current (Icc)						
1.5 to 49.999 MHz	-	-	20	mA		
50 to 79.999 MHz	-	-	30	mA		
80 to 124.999 MHz	-	-	40	mA		
125 to 160.999 MHz		-	50	mA		

Input Characteristics

Parameter	Minimum	Nominal	Maximum	Units	Notes	
Enable Voltage - (Vih)	≥70% Vdd	-	-	Vdc	2	
Disable Voltage - (Vil)	-	-	≤ 30% Vdd	Vdc		
Enable Time	-	-	10	mS		
Disable Time	-	-	150	nS		
Output Disable Current (Icc)	-	-	10	uA		

HCMOS Output Characteristics

Parameter	Minimum	Nominal	Maximum	Units	Notes
Load	-	-	15	pF	
Voltage High (Voh)	2.70	-	-	Vdc	
Low (Vol)	-	-	0.36	vuc	
Current High (loh)	-2	-	-	mA	
Low (IoI)	-	-	2	ШA	
Duty Cycle 1.5 to 49.999 MHz	45	50	55	%	3
50 to 160 MHz	40	50	60	70	3
Rise / Fall Time 1.5 to 79.999 Mh	Z -	-	6	nS	4
80 to 124.999 MI	nz -	-	4	nS	4
125 to 160 MHz	-	-	3	nS	4
Start-Up Time	-	-	10	mS	
Jitter	-	-	5	pS RMS	

Notes:

1. Inclusive of calibration @ 25°C, frequency vs temperature stability, supply voltage change, load change, shock and vibration, 10 years aging.

2.. Oscillator output is enabled with no connection on pad 1

Duty Cycle measured at 50% of Vcc.
Rise and Fall times measured from 10% to 90%.



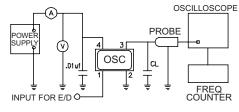
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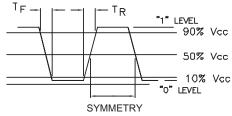
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		age Chara			
Package	Hermetically	v sealed ceran	nic package an	d metal cover	- <u>-</u> (A
Temperature C	Cycle: The specient tested 5 cycles	men shall mee s of -55°C / 30	minutes and +	racteristics after 125°C / 30 minutes	POWER SUPPLY V
Hermetical: Solvent Resista	nce: Marki	ng will withsta	nd immersion ir Trichloroethylen		
		Solderin	q		
General Cond	230	°C max x 180	max x 2 times r sec max x 1 tim		O l
Typical Opera	20 to 10		w) 5°C, 50 sec at nperature per 1		
	Mecha	anical Char	acteristics		
Free Drop:	Free Drop testing	on the hard w	ooden board fr	cs after tested 3 times, om a height of 75 cm.	
Vibration:	by the following	conditions: 1	0-55Hz 1.5mm	ristics after tested Amplitude,	0
		nermal Shock	of 260°C max x	10 sec max x 2 times, electrical characteristics	_ Sugg
Solderability:	(EIAJ-RCX-	0102.101 Con	dition 1a)		
	-F-14256 (WW Ros Q-S-571 (Sn = 639		ropyl Alcohol =	75%)	- 100 (2.54 mm
3) Solder ba	ath temperature: 23	35°C ±5°C			
	immersion: Up to e g time: Within 2 se				.055
After perfor	ming the above pro			overage shall be	(1.4m Bypas
greater thar		Le de la Le Ce			– be cer
	Un	dering Infor			Pa
				050.0M	Pad C
Type Clock	Temperature Range	Frequency Tolerance	Supply Voltage	Output Frequency Frequency Format	1:
3.2x5.0mm Package	1 = 0 to 70°C 4 = -20 to 70°C	$1 = \pm 25 \text{ ppm}$ $2 = \pm 50 \text{ ppm}$	3 = 3.3 Vdc	-xxx.xM Minimum	2:
Tuokugo		$3 = \pm 100 \text{ ppm}$ $4 = \pm 20 \text{ ppm}$		*Amount of numbers after the decimal point. M = MHz	4:
	44.7	4 MHz = 7113-0 36 MHz = 7113-	044.736M	ey of:	Pa
T		25 MHz = 7113-	125.0M		
Tape and	Reel Dimensio				Hz 6B
8.46 DI/ (215mm		217_ (5.5mm) .08 .0mm) .15 (2.7mm)	.157 (4.0mm)		.205 MAX (5.2 Imm) 200MHZ
1.00 DIA (25mm)	9.84 (250mm) 2.0mm) 3.15 (80mm)	(3.2mm) 3.15 (80mm) C (80mm) C C C C C C C C C C C C C			Dimensional (.508mm) (.127mm)
Meets EIA-48 ⁻ 2,000 PCS/RE	IA and EIAJ-1009B EL		(1.75mm) .47 (12.0mm)		

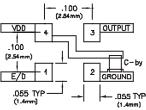
Test Circuit



Dutput Waveform



Suggested Pad Layout



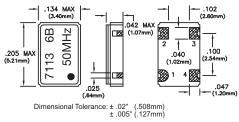
Bypass capacitor, C-by, should be ceramic capacitor \geq .01uf.

Pad Connections

Pad Connection

1:	Enable / Disable
2:	Ground
3:	Output
4:	Vcc

Package Outline



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Specifications subject to change without notification. See Connor-Winfield's website for latest revision. All dimensions in inches. © Copyright 2016 The Connor-Winfield Corporation Not intended for life support applications.