5.0V Surface Mount 5.0mm x 7.5mm Oscillators VKA61A5, VKA62A5, VKA63A5



2111 Comprehensive Drive

Aurora, Illinois 60505

Phone: 630-851-4722

Fax: 630-851-5040

www.conwin.com

US Headquarters: 630-851-4722 European Headquarters: +353-61-472221

VCXO

The Connor-Winfield, RoHS compliant, VKA61A5, VKA62A5, and VKA63A5 are hermetically sealed, Surface Mount, 5.0V Voltage Controlled Crystal Oscillators (VCXO) with the enable/disable function on pad 5. The VKA61A5, VKA62A5, and VKA63A5 are designed for phased lock loop applications requiring low jitter and tight stability. The surface mount package is designed for high-density mounting and is optimum for mass production.

Features:

RoHS Compliant 5.0V Operation

Overall Frequency Tolerance:

VKA61A5: ±25ppm VKA62A5: ±50ppm VKA63A5: ±100ppm

Low Jitter <1pS RMS

Temperature Range -40° to 85°C

Enable / Disable Pad 5

Leadless Surface Mount Package Tape and Reel Packaging

Absolute Maximum Ratings

Parameter	Minimum	Nominal	Maximum	Units	Notes
Storage Temperature	-55	-	125	°C	
Supply Voltage (Vcc)	-0.5	-	7.0	Vdc	
Control Voltage (Vc)	-0.5	-	7.0	Vdc	

Operating Specifications

	Operating	gopeemean	0113		
Parameter	Minimum	Nominal	Maximum	Units	Notes
Frequency Range (Fo)	1.0	-	52	MHz	
Frequency Tolerance Model VKA61A5 Model VKA62A5 Model VKA63A5	-25 -50 -100	- - -	25 50 100	ppm	1
Operating Temperature Range	-40	-	85	°C	
Supply Voltage (Vcc)	4.75	5.0	5.25	Vdc	
Supply Current (Icc) 1.0 to 19.999 MHz 20 to 52 MHz	- -	- -	15 25	mA	
Jitter: (BW=12kHz to 20 MHz) (BW=10Hz to 20 MHz)	-	- -	1 5	ps RMS	

Input Characteristics

Parameter	Minimum	Nominal	Maximum	Units	Notes
Control Voltage Range (Vc)	0.5	2.5	4.5	Vdc	
Frequency Pullability @ 25°C	±100	-	-	ppm	
Monotonic Linearity	-10	-	10	%	
Input Impedance	-	50K	-	Ohm	
Modulation Bandwidth (3dB)	10	-	-	kHz	
Enable Input Voltage - High (Vih Disable Input Voltage - Low (Vil)	2.7	-	0.3	Vdc	2

HCMOS Output Characteristics

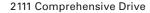
Parameter	Minimum	Nominal	Maximum	Units	Notes
Load	-	-	15	pf	
Voltage High (Voh) Low (Vol)	4.5 -	- -	0.5	Vdc	
Current High (loh) Low (lol)	-4 -		- 16	mA	
Duty Cycle at 50% of Vcc	45	50	55	%	
Rise / Fall Time 10% to 90%	-	-	5	nS	

RoHS

Bulletin	Vx272
Page	1 of 2
Revision	07
Date	14 Sept 2007

Notes:

- 1. Referenced to (Fo) measured with control voltage @ 2.5Vdc. Inclusive of frequency vs. temperature stability, supply voltage change, load change, shock and vibration, 15 years aging.
- 2. The Output is enabled with no connection on the enable pin.



Aurora, Illinois 60505

Phone: 630-851-4722 Fax: 630-851-5040

www.conwin.com

Package Characteristics

Package Hermetically sealed, ceramic leadless package.

Environmental Characteristics

The specimen shall meet electrical characteristics after tested 5 cycles of -55°C / 30 minutes and +125°C / 30 minutes Temperature Cycle

Hermetical No bubbles appear in Flourinert (FC-43) at 125°C ±5°C for 5 minutes

Marking will withstand immersion in Isopropyl Alcohol or Trichloroethylene Solvent Resistance

Soldering

260°C max x 10 sec max x 2 times max or 230°C max x 180 sec max x 1 time General Conditions

20 to 100 sec up to 215°C, 50 sec at 215°C, then down to room temperature per 1 to 5°C / sec Typical Operation Data (Vapor phase reflow)

Mechanical Characteristics

The specimen shall meet electrical characteristics after tested 3 times, Free Drop testing on the hard wooden board from a height of 75 cm. Free Drop

Vibration The specimen shall meet electrical characteristics after tested by the following conditions: 10-55Hz 1.5mm Amplitude, 55-2000 Hz 20 G's, 2 hours for each plane

Thermal Shock After applied Thermal Shock of

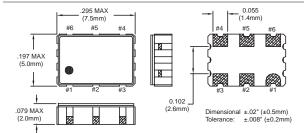
245°C max x 10 sec max x 2 times, or 215°C max x 180 sec max, the specimen shall meet electrical characteristics

Solderability

(EIAJ-RCX-0102/101 Condition 1a)
Flux: MIL-F-14256 (WW Rosin=25%, Isopropyl Alcohol = 75%)
Solder: QQ-S-571 (Sn = 63%, Pb = 37%)
Solder bath temperature: 235°C ±5°C

4) Depth of immersion: Up to electrical terminal 5) Immersing time: Within 2 sec ±0.5 sec into solder bath

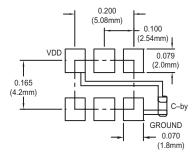
After performing the above procedures, a newly soldered coverage shall be greater than 90%



Pin Function

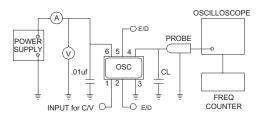
- 1: Control Voltage
- 2: N/C
- Ground 3:
- 4: Output
- 5: E/D
- VDD

Suggested Pad Layout

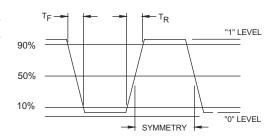


Bypass capacitor, C-by, should be ceramic capacitor ≥ .01uf

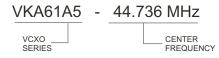
Test Circuit



Output Waveform



Ordering Information



Tape and Reel Dimensions		
.69 (17.5mm)	7.31 08 (7.9mm)	VKA
7.48 DIA (2.0mm) 9.84 DIA (250mm) 9.84 DIA (250mm) 3.15 (8.0mm) (21mm) 0.339 R (1.0mm)	.315 (8.0mm) O .06 DIA (1.5mm) O .295 (7.5mm)	VCXO SERIE
MEETS EIA—481A and EIAJ—1009B 2,000 PCS/REEL	.07 (1.75mm) Dimensional	Tolerance: ±.02* (.508mm) ±.005* (.127mm)

Bulletin	Vx272
Page	2 of 2
Revision	07
Date	14 Sept 2007