3.3V Surface Mount 3.2mm x 5.0mm Oscillators V7323 & V7333 Series



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VCXO

Description

The Connor-Winfield, RoHS compliant, V7323 and V7333 are hermetically sealed, Surface Mount, 3.3V Voltage Controlled Crystal Oscillators (VCXO) with Tri-State Enable/Disable function on pad 6. The V7323 and V7333 are designed for phased lock loop applications requiring low jitter and tight stability.



Features:

- RoHS Compliant
- 3.3V Operation
- Small Surface Mount Package: 5.0mm x 3.2mm x 1.2mm
- Overall Frequency Tolerance: V7323: ±50ppm V7333: ±100ppm
- Low Jitter <1pS RMS
- Temperature Range -10° to 85°C
- Enable / Disable Pad 6
- Tape and Reel Packaging

Absolute Maximum Ratings

| Parameter | Minimum | Nominal | Maximum | Units | Notes |
|----------------------|---------|---------|---------|-------|-------|
| Storage Temperature | -55 | - | 125 | °C | |
| Supply Voltage (Vcc) | -0.5 | - | 7.0 | Vdc | |
| Control Voltage (Vc) | -0.5 | - | Vcc | Vdc | |

Operating Specifications

| oporating opoontoatione | | | | | | | |
|----------------------------------|----------------|---------|---------|--------|-------|--|--|
| Parameter | Minimum | Nominal | Maximum | Units | Notes | | |
| Frequency Range (Fo) | 2.0 | - | 52 | MHz | | | |
| Frequency Tolerance | | | | | | | |
| Model V7323 | -50 | - | 50 | ppm | 1 | | |
| Model V7333 | -100 | - | 100 | | | | |
| Operating Temperature Range | -10 | - | 85 | °C | | | |
| Supply Voltage (Vcc) | 3.135 | 3.3 | 3.465 | Vdc | | | |
| Supply Current (Icc) | | | | | | | |
| 1.0 to 29.999 MHz | - | - | 15 | mA | | | |
| 30 to 52 MHz | - | - | 25 | | | | |
| Jitter: | | | | | | | |
| Integrated Phase Jitter (BW=12kH | z to Fo/2 MHz) | | 1 | ps RMS | | | |
| Period Jitter | - | - | 5 | | | | |

Input Characteristics

| Parameter | Minimum | Nominal | Maximum | Units | Notes | |
|-----------------------------------|---------|---------|---------|-------|-------|--|
| Control Voltage Range (Vc) | 0.15 | 1.65 | 3.15 | Vdc | | |
| Frequency Pullability @ 25°C | ±100 | - | - | ppm | | |
| Monotonic Linearity | -10 | - | 10 | % | | |
| Input Impedance | - | 50K | - | Ohm | | |
| Modulation Bandwidth (3dB) | 10 | - | - | KHz | | |
| Enable Input Voltage - High (Vih) | 0.7Vcc | - | - | Vdc | 2 | |
| Disable Input Voltage - Low (Vil) | - | - | 0.3 | | | |

LVCMOS Output Characteristics Minimum Nominal Maximum Units Parameter Notes Load 15 pf 2.97 Voltage High (Voh) Vdc 0.33 Low (Vol) Current High (loh) -1 mΑ --Low (IoI) 4 Duty Cycle at 50% of Vcc 40 50 60 % Rise / Fall Time 20% to 80% 5 nS --Start-up Time --10 mS

Notes:

Referenced to (Fo) measured with control voltage @ 2.5Vdc. Inclusive of frequency vs. temperature stability, supply voltage, load change, shock and vibration, 15 years aging.
 The Output is enabled with no connection on the enable pin. Output is at high impedance when disabled.



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| Revision | 02 |
| Date | 04 June 2015 |



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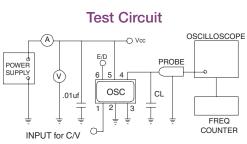
> .040 (1.0mm)

> > .043 (1.1mm)

<u>.043</u> (1.1mm

| | | Package Characteristics | Sugg | jested F |
|---|---|---|-----------------|-----------------|
| | Package | Hermetically sealed, ceramic leadless package. | | |
| - | Temperature Cycle | Environmental Characteristics The specimen shall meet electrical characteristics after tested 5 cycles of -55°C / 30 minutes and +125°C / 30 minutes | .040 (1.0mm) | 060 (1.5mm) |
| - | Hermetical | No bubbles appear in Flourinert (FC-43) at $125^{\circ}C \pm 5^{\circ}C$ for 5 minutes | .040 | |
| - | Solvent Resistance | Marking will withstand immersion in Isopropyl Alcohol or Trichloroethylene | (1.0mm) | |
| - | General Conditions Typical Operation Dat (Vapor phase reflow) | Soldering 260°C max x 10 sec max x 2 times max or 230°C max x 180 sec max x 1 time a 20 to 100 sec up to 215°C, 50 sec at 215°C, then down to room temperature per 1 to 5°C / sec | | .130 (3.3mm) |
| | | Mechanical Characteristics | | |
| | Free Drop | The specimen shall meet electrical characteristics after tested 3 times, Free Drop testing on the hard wooden board from a height of 75 cm. | | Test C |
| | Vibration The sp | becimen shall meet electrical characteristics after tested by the following conditions: 10-55Hz 1.5mm Amplitude, 55-2000 Hz 20 G's, 2 hours for each plane | | E/D O |
| - | Thermal Shock | After applied Thermal Shock of 245°C max x 10 sec max x 2 times, or | | 6 5 4 |

Suggested Pad Layout



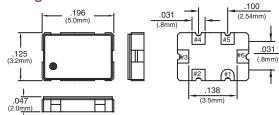
215°C max x 180 sec max, the specimen shall meet electrical characteristics EIAJ-RCX-0102/101 Condition 1a) 1) Flux: MIL-F-14256 (WW Rosin=25%, Isopropyl Alcohol = 75%)

- 2) Solder: QQ-S-571 (Sn = 63%, Pb = 37%)
- 3) Solder bath temperature: 235°C ±5°C
- 4) Depth of immersion: Up to electrical terminal
- 5) Immersing time: Within 2 sec ± 0.5 sec into solder bath

After performing the above procedures, a newly soldered coverage shall be greater than 90%

Package Outline

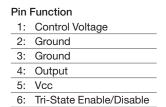
Solderability



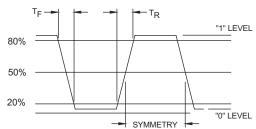
2,000 PCS/REEL

Pad Connection

- .47 (12.0mm)



Output Waveform



Ordering Information

V7323 - 50.00 MHz

vcxo

±.02" (.508mm) ±.005" (.127mm) SERIES

Tape and Reel Dimensions PIN 1 .53 _ (13.5mm) .217_ (5.5mm) .08 _ (2.0mm) .08 8.46 DIA (215mm DIA) (2.0mm) .15 (3.7mm) ł 9.84 DIA 4 (250mm DIA) .157 0 (4.0mm) .08 0 (2.0mm 3.15 3.15 (8.0mm) (8.0mm) - .06 DIA 0 (1.5mm DIA) ł 1.00 DIA Q Dimensional Tolerance (25mm DIA) .217 (5.5mm) _.07 (1.75mm) MEETS EIA-481A and EIAJ-1009B

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FREQUENCY

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