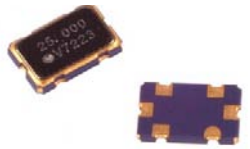
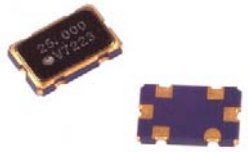
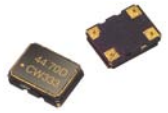
Jitter generated from clock sources is becoming an increasingly important issues for designers of high bandwidth communication networks. As Ethernet and Optical network technologies increase in speed and bandwidth to 40/100/400G, ultra low jitter clock generation is required. Ultra-low jitter clocks offer significant improvement in signal-to-noise ratio which improves overall system performance. Connor Winfield has designed a number of ultra low jitter clock and VCXO offerings based on its crystal expertise in high frequency fundamental crystal technology and overtone crystal technology that support these critical requirements. Follow the links below for information on specifications and stock availability.

Ultra Low Jitter Product Line Summary VCXO Products



Series	VT8 Series	VB762	V788	V7xx3, V7xx5
SMD Footprint Dimensions	5x7mm	5x7mm	5x7mm	5x3.2mm
Stability Range	±25ppm	No stability specified, APR	No stability specified, APR	±50ppm
Frequency Range	50MHz to 160MHz	70MHz to 170 MHz	200MHz to 800MHz	10MHz to 52MHz
Output Options	CMOS	LVPECL	LVPECL	LVCMOS/HCMOS
Voltage Options	3.3V	3.3V	3.3V	3.3V or 5.0V
Phase Jitter (12k - 20MHz)	<100fs	100fs typical at 155.52MHz	<100fs	<1ps RMS 12kHz to Fo/2
Temperature Range	-40°C to 85°C	-40°C to 85°C	-40°C to 85°C	-40°C to 85°C
Data Sheet Link	VT Series	VB762	V788	V7xx3
Data Sheet Link				V7xx5

Ultra Low Jitter Product Line Summary XO Products

Series	PB Series	721x Series	CW3 Series
SMD Footprint Dimensions	5x7mm	5x3.2mm	3.2 x 2.5mm
Stability Range	±50ppm	±25ppm	±25ppm
Frequency Range	70MHz to 170MHz	10MHz to 100MHz	10MHz to 100MHz
Output Options	LVPECL	CMOS	CMOS
Voltage Options	3.3V	2.5V or 3.3V	2.5V or 3.3V
Phase Jitter (12k - 20MHz)	<100fs	<300fs	<300fs
Temperature Range	-40°C to 85°C	-40°C to 85°C	-40°C to 85°C
Data Sheet Link	BP Series	72x3 Series	CW3 Series
Data Sheet Link		72x5 Series	