CONNOR WINFIELD

NEWS RELEASE

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Connor-Winfield's New Synchronous Clock Generators Significantly Increase Bandwidth and Reduce Phase Gain

Connor-Winfield introduces two new Synchronous Clock Generators – the SCG2000 and SCG2500. Both are mixed-signal phase locked loops generating LV CMOS outputs from a fundamental frequency, low jitter, voltage controlled, crystal oscillator. They provide high precision frequency translation for telecommunications applications.

Enhanced Performance: The SCG2000 and SCG2500 offer significant improvements in performance, especially in applications where network line cards using these SCGs are cascaded together in series. Enhanced performance includes lower phase gain for lower percent overshoot and greater bandwidth.

Additional benefits include smoother switching into Free Run mode, closer tracking of signal "wander," better isolation between inputs and outputs, and better temperature transient performance.

Features of the new SCG2000 and SCG2500 include:

- Phase Locked Output Frequency Control
- Intrinsically Low Jitter Crystal Oscillator
- One or Two Selectable References @ 8 kHz
- Tri-Stateable Oscillator and Alarm Outputs
- Fast .35 Second Acquisition Time
- 3.3V Power Supply
- Surface Mount, DIL Package
- Low Voltage CMOS and TTL Compatible Outputs
- Frequency Range: 1.544 – 125 MHz

Products are available for order as of May 2001.

For more information contact Connor-Winfield Corp.
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www.conwin.com
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Data Sheet Link: **SCG2000: www.conwin.com/datasheets/sg/sg029.pdf**
SCG2500: www.conwin.com/datasheets/sg/sg027.pdf

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