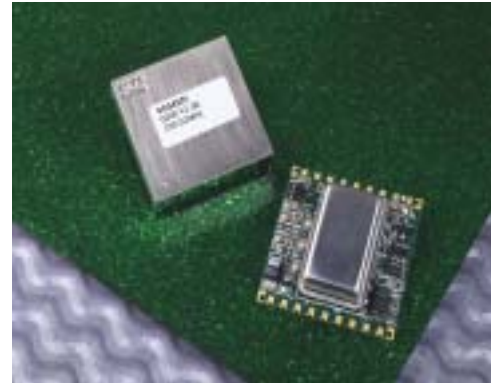


August, 2002

The New Connor-Winfield SCG4521 adds a Secondary Output to the SCG4500 Product Family

The new SCG4521 from Connor-Winfield model has a primary LVPECL OC-3 output and a secondary (OC1) CMOS Reference Output. Both outputs have a maximum 40/60 duty cycles and are phase locked to one of two selected input references. The SCG4521 phase locked loop relies on a fundamental frequency, low jitter, voltage controlled, crystal oscillator; as is the case with all SCG4500 series products. They are ideally suited for SONET/SDH line cards in network element equipment operating from OC-1 to OC-48.



Enhanced Performance: The SCG4521 also features < 1 ps RMS jitter, < .2 dB phase gain, hitless reference switching and 20 ppm Free Run.

The unit has a fast acquisition time of 1.5 seconds and is tolerant of reference inputs signals that have different duty cycles. In addition, smooth switching into Free Run mode, close tracking of signal “wander,” better isolation between inputs and outputs, and better temperature transient performance help to increase overall system performance.

Features of the new SCG4521 include:

- Phase Locked Output Frequency Control
- Intrinsic Low Jitter OC-3 Output
- Primary LVPECL Differential Output (@155.52 MHz) with Disable Function
- Secondary CMOS Output @ 51.84 MHz
- Two Selectable References @ 19.44 MHz
- 20 ppm Free Run Operation
- One Alarm Output Indicates LOR/LOL
- 3.3V DC Power Supply
- Small size: 1 Square Inch

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